

NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING AND RESEARCH, CHANDIGARH

(Deemed to be University under Distinct Category)

Date-sheet for End-Semester Examination, Batch-2025

B.Tech. (Lateral entry) for Working Professional Programmes, 3rd Semester and ME ECE (VLSI Design), 1st Semester

Time of Examination: 10.00 AM to 01.00 PM / 1.30 PM Venue: Examination Hall, Academic Cell Building

Day & Date	B.Tech. (Mechanical Engineering)	B.Tech. (Civil Engineering)	B.Tech. (Electronics and Communication Engineering)	M.E. (Electronics and Communication Engineering) (VLSI Design)
10.01.2026 (Saturday)	(104003): Manufacturing Process-1	(024003): Engineering Mathematics	(074001) Signal and System	-----
11.01.2026 (Sunday)	(104061): Computer Aided Design & Drafting	(024002): Fluid Mechanics	(074003): Electronic Device & Circuits	-----
16.01.2026 (Friday)	-----	(024001): Theory of Structures	(074004): Electromagnetic Field Theory (MOOC) (10.00 AM to 11.30 AM)	(072102): Analog IC Design
17.01.2026 (Saturday)	(104002): Mechanics of Materials	(024061): Computer Aided Drawing	(074002): Digital Electronics	(072101): Semiconductor Devices
18.01.2026 (Sunday)	(104001): Material Science (10.00 AM to 11.30 AM)	(024042): Mechanics of Material (MOOCs) (10.00 AM to 11.30 AM)	(074041): Environmental Science (MOOCs) (10.00 AM to 11.30 AM)	(072103): Hardware Description Language
	(104071): Basic Environmental Engineering (12.00 Noon to 1.30 PM)	(024041): Sustainable Transportation System (MOOCs) (12.00 Noon to 1.30 PM)	(074091): Electrical Measurement & Electronic Instruments Circuits (12.00 Noon to 1.30 PM)	-----

**NOTE: TIMING FOR MOOCs EXAMINATIONS: 10.00 AM to 11.30 AM for FIRST SHIFT
12.00 Noon to 01.30 PM for SECOND SHIFT.**

Hemant K...
07/01/2026
Controller of Examinations